

L&T Semiconductor Technologies Inks MoU with C-DAC To Take C-DAC's Tech Solutions to Global Market

Mumbai, August 28, 2024: L&T Semiconductor Technologies (LTSCT) a pioneer in indigenous semiconductor design & development, has signed a Memorandum of Understanding (MoU) with the Centre for Development of Advanced Computing (C-DAC), an autonomous scientific entity under the Ministry of Electronics and Information Technology (MeiTY), Government of India.

The strategic collaboration is to drive indigenisation efforts and harness the collective expertise of both organisations to accelerate innovation and technological advancements in areas of mutual interest.

The MoU delineates a comprehensive framework for collaborative research, development, and training initiatives, with an emphasis on the creation of Make-in-India Integrated Circuit (IC) / System-on-Chip (SoC) and Electronics System Design & Manufacturing (ESDM) solutions for automotive, industrial, and energy applications.

The opportunities to strengthen India's economic foundation through indigenous semiconductor design & development are immense, particularly by reducing reliance on electronic imports from abroad. The collaborative efforts in developing specialised semiconductor technologies will be pivotal in advancing Indian processor cores, Intellectual Properties, and SoCs – the most cost-intensive components within the semiconductor value chain.

Congratulating both the organisations, Dr Sunita Verma, Group Coordinator, (R&D E&IT) from the Ministry of Electronics and Information Technology (MeiTY), said: “The signing of this MoU between L&T Semiconductor Technologies and C-

while significantly enhancing the nation's technological capabilities and positioning India as a leader in each of the sectors .”

Mr Magesh E, Director General - C-DAC, highlighted that the collaboration will significantly boost the semiconductor industry and strengthen the Digital India RISC-V (DIR-V) ecosystem.

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